

EAST SEARCH

8/25/05

L#	Hits	Search String	Databases
S1	607709	(integrated or digital) near2 circuit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S2	50401	(sub-micron near2 ASIC) or (million near2 gate\$1 near2 ASIC) or ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S3	618008	S1 or S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S4	4286	S3 and ((predict\$3 or estimat\$3 or calculat\$3) near2 delay\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S5	9130	S3 and ((predict\$3 or estimat\$3 or calculat\$3) with delay\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S6	1254	S5 and ((predict\$3 or estimat\$3 or calculat\$3) with (parallel or distribut\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S7	45	S6 and (partition\$3 with (netlist\$1 or circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S8	1	S6 and (logic with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S9	54	S6 and (timing with independent\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S10	1	S6 and ((global or design) with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S11	72	S6 and (global with clock\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S12	0	S6 and (clock with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S13	1	S6 and (gate with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S14	153	S6 and (clock with network\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S15	7	S6 and (form\$3 with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S16	87	S6 and (logic with control\$3 with clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S17	2	S6 and (script with delay\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S18	0	S6 and (script\$1 with "output file")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S19	6	S6 and (script\$1 with file)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S20	7	S6 and (delay\$1 with (output near2 file))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S21	296	S6 and (delay\$1 with (computer\$1 or processor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S22	0	S6 and (writer with cone\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S23	5	S6 and (write with netlist\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S24	0	S6 and ((clock with network\$1) with multithreading)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S25	0	S6 and ((clock with network\$1) with multi-threading)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S26	0	S6 and (cone\$1 with multi-threading)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S27	4	S6 and (multi-threading)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S28	226	S7 or S8 or S9 or S10 or S11 or S13 or S15 or S16 or S17 or S19 or S20 or S23 or S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S29	381	S14 or S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S30	103	S28 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S31	226	S28 or S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S32	89	S2 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S33	0	S31 and (sub-micron near2 ASIC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S34	607709	(integrated or digital) near2 circuit\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S35	50401	(sub-micron near2 ASIC) or (million near2 gate\$1 near2 ASIC) or ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S36	618008	S34 or S35	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

S37
S38
S39

9130 S36 and ((predict\$3 or estimat\$3 or calculat\$3) with delay\$1)
1254 S37 and ((predict\$3 or estimat\$3 or calculat\$3) with (parallel or distribut\$3))
7 S38 and (form\$3 with cone\$1)

L1
L2
L3
L4

648398 (integrated or digital) near2 circuit\$1
9586 1 and ((predict\$3 or estimat\$3 or calculat\$3) with delay\$1)
1292 2 and ((predict\$3 or estimat\$3 or calculat\$3) with (parallel or distribut\$3))
1 3 and ((logic or design or clock) with cone\$1)

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Results of search set S51:

Document Kind	Codes	Title	Issue Date	Current OR	Abstract
US	20050044515 A1	Method for determining and using leakage current sensitivities to optimize the design of an inte	20050224	716/5	
US	20040223196 A1	Image sensor Unit	20041111	358/514	
US	20040196514 A1	Image sensor unit	20041007	358/514	
US	20040190092 A1	Scanning device for coded data	20040930	358/539	
US	20040174576 A1	Color signal compensation	20040909	358/514	
US	20040167756 A1	Apparatus and method for statistical LSI delay simulation	20040826	703/2	
US	20040130349 A1	Logic circuit and method of logic circuit design	20040708	326/112	
US	20040068599 A1	Global interrupt and barrier networks	20040408	710/260	
US	20040017846 A1	Non-parametric matched filter receiver for wireless communication systems	20040129	375/152	
US	20040015079 A1	Ultrasound probe with integrated electronics	20040122	600/437	
US	20040006584 A1	Array of parallel programmable processing engines and deterministic method of operating the	20040108	718/107	
US	20030229480 A1	Pseudo dynamic current estimating for circuits	20031211	703/14	
US	20030223433 A1	Internet protocol system using hardware protocol processing logic and parallel data processing	20031204	370/395.52	
US	20030176787 A1	Ultrasound probe with integrated electronics	20030918	600/437	
US	20030138030 A1	Configurable terminal engine	20030724	375/141	
US	20030118177 A1	Method and system for implementing a reduced complexity dual rate echo canceller	20030626	379/406.01	
US	20030112966 A1	Method and system for implementing a reduced complexity dual rate echo canceller	20030619	379/406.05	
US	20030112887 A1	Method and system for implementing weighted vector error echo cancellers	20030619	375/285	
US	20030112861 A1	Method and system for adaptively training time domain equalizers	20030619	375/232	
US	20030112860 A1	Method and system for shortening channel impulse response using time domain equalization fil	20030619	375/229	
US	20030101307 A1	System of distributed microprocessor interfaces toward macro-cell based designs implemente	20030529	710/305	
US	20030073894 A1	Ultrasound probe with integrated electronics	20030417	600/407	
US	20030058164 A1	Methods and systems for improvement of measurement efficiency in surveying	20030327	342/357.17	
US	20030048811 A1	Methods, systems and computer program products for synchronizing clocks of nodes on a con	20030313	370/509	
US	20030011609 A1	Graphics system with real-time convolved pixel readback	20030116	345/582	
US	20020141478 A1	Apparatus for initial power control for spread-spectrum communications	20021003	375/130	

US 20020120193 A1	Ultrasound probe with integrated electronics	20020829 600/439
US 20020114270 A1	Multiplex communication	20020822 370/208
US 20020097885 A1	Acoustic source localization system and method	20020725 381/92
US 20020067359 A1	Unitary operator control for ultrasonic imaging graphical user interface	20020606 345/440
US 20020061050 A1	Method for initial power control for spread-spectrum communications	20020523 375/141
US 20020057659 A1	Apparatus for adaptive forward power control for spread-spectrum communications	20020516 370/335
US 20020057446 A1	MULTI-INSTRUCTION STREAM PROCESSOR	20020516 358/1.13
US 20020051482 A1	Median weighted tracking for spread-spectrum communications	20020502 375/141
US 20020051434 A1	Method for using rapid acquisition spreading codes for spread-spectrum communications	20020502 370/335
US 20020044539 A1	Centroid tracking for spread-spectrum communications	20020418 370/335
US 20020036998 A1	Adaptive vector correlator for spread-spectrum communications	20020328 370/342
US 20020036996 A1	Initial power control for spread-spectrum communications	20020328 370/335
US 20020034169 A1	Apparatus for adaptive reverse power control for spread-spectrum communications	20020321 370/335
US 20020034167 A1	Method for adaptive forward power control for spread-spectrum communications	20020321 370/329
US 20020027946 A1	Method for adaptive reverse power control for spread-spectrum communications	20020307 375/130
US 20020021686 A1	System for using rapid acquisition spreading codes for spread-spectrum communications	20020221 370/342
US 20010055320 A1	Multiplex communication	20011227 370/480
US 6859831 B1	Method and apparatus for internetworked wireless integrated network sensor (WINS) nodes	20050222 709/224
US 6832251 B1	Method and apparatus for distributed signal processing among internetworked wireless integra	20041214 709/224
US 6826607 B1	Apparatus for internetworked hybrid wireless integrated network sensors (WINS)	20041130 709/224
US 6823294 B1	Method and system for measuring circuit design capability	20041123 703/2
US 6816473 B2	Method for adaptive forward power control for spread-spectrum communications	20041109 370/335
US 6795076 B2	Graphics system with real-time convolved pixel readback	20040921 345/502
US 678662 B2	Method for adaptive reverse power control for spread-spectrum communications	20040907 370/335
US 6783493 B2	Ultrasound probe with integrated electronics	20040831 600/437
US 6772181 B1	Apparatus and method for trigonometric interpolation	20040803 708/313
US 6735630 B1	Method for collecting data using compact internetworked wireless integrated network sensors (20040511 709/224
US 6721301 B2	Centroid tracking for spread-spectrum communications	20040413 370/342
US 6707805 B2	Method for initial power control for spread-spectrum communications	20040316 370/335
US 6697350 B2	Adaptive vector correlator for spread-spectrum communications	20040224 370/342
US 6681373 B1	Method and apparatus for dynamic buffer and inverter tree optimization	20040120 716/2
US 6674536 B2	Multi-instruction stream processor	20040106 358/1.15
US 6669633 B2	Unitary operator control for ultrasonic imaging graphical user interface	20031230 600/437
US 6665308 B1	Apparatus and method for equalization in distributed digital data transmission systems	20031216 370/441
US 6661775 B1	Redundant routing with deadlines in data networks	20031209 370/230.1
US 6637011 B1	Method and apparatus for quick search for identities applicable to specified formula	20031021 716/3
US 6633256 B2	Methods and systems for improvement of measurement efficiency in surveying	20031014 342/357.17
US 6622291 B1	Method and apparatus for physical budgeting during RTL floorplanning	20030916 716/9
US 6587590 B1	Method and system for computing 8.times.8 DCT/IDCT and a VLSI implementation	20030701 382/250
US 6564361 B1	Method and apparatus for timing driven resynthesis	20030513 716/8
US 6543032 B1	Method and apparatus for local resynthesis of logic trees with multiple cost functions	20030401 716/2
US 6532582 B1	Method and apparatus for optimal critical netlist area selection	20030311 716/11
US 6530887 B1	Ultrasound probe with integrated electronics	20030311 600/459

US 6467092 B1	Method for adjusting power in a communication system with multicarrier telephony transport	20021015 725/131
US 6456608 B1	Adaptive vector correlator using weighting signals for spread-spectrum communications	20020924 370/335
US 6425114 B1	Systematic skew reduction through buffer resizing	20020723 716/6
US 6253358 B1	Method for supporting the design of semiconductor integrated circuit and system using the same	20010626 716/6
US 6230303 B1	Proximity-based cluster allocation for hardware-software co-synthesis of heterogeneous distributed	20010508 716/7
US 6205570 B1	Method for designing LSI circuit pattern	20010320 716/1
US 6117180 A	Hardware-software co-synthesis of heterogeneous distributed embedded systems for low power	20000912 703/20
US 6099580 A	Method for providing performance-driven logic optimization in an integrated circuit layout design	20000808 716/7
US 6097886 A	Cluster-based hardware-software co-synthesis of heterogeneous distributed embedded system	20000801 703/23
US 6086628 A	Power-related hardware-software co-synthesis of heterogeneous distributed embedded system	20000711 716/7
US 5991308 A	Lower overhead method for data transmission using ATM and SCDMA over hybrid fiber coaxial	19991123 370/395.53
US 5873054 A	Method and apparatus for combinatorial logic signal processor in a digitally based high speed system	19990216 702/190
US 5838947 A	Modeling, characterization and simulation of integrated circuit power behavior	19981117 703/14
US 5737447 A	Device for hierarchical estimation of the motion of image sequences	19980407 382/236
US 5678521 A	System and methods for electronic control of an accumulator fuel system	19971021 123/447
US 5630109 A	Apparatus for processing of a series of timing signals	19970513 713/501
US 5428754 A	Computer system with clock shared between processors executing separate instruction streams	19950627 712/220
US 5329630 A	System and method using double-buffer preview mode	19940712 711/173
US 5056015 A	Architectures for serial or parallel loading of writable control store	19911008 703/27
US 5042000 A	Integral transform method	19910820 708/404